

Seiya Ono

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Education

University of California, Berkeley

August 2015 - May 2020

Bachelor of Science - Electrical Engineering & Computer Sciences

GPA: 3.76

Master of Science - Electrical Engineering & Computer Sciences

GPA: 3.89

Relevant Coursework:

Data Structures & Algorithms, Machine Structures, Microelectronic Devices & Circuits, Discrete Mathematics & Probability Theory, Operating Systems & System Programming, Signals & Systems, Micro-fabrication Technology, Integrated-Circuit Devices, Digital Design and ICs, MEMS, Photovoltaic Devices, Solid State Devices, Embedded Systems, Introduction to Multiferroic Devices

Relevant School Projects:

Three stage MOSFET Small Signal Amplifier, PintOS Thread Scheduling/User Programs/File System, Microscale Device Fabrication & Characterization, FPGA implemented RISC-V CPU integrated with memory mapped UART, I2S, and VGA, Device Optimizations for 20nm Bulk & FDSOI MOSFETs, Bare Metal Hardware Implemented Bop-It! Rhythm game

Employment

Devices & Systems - Head Lab Teacher Assistant:

January 2017 - August 2020

Facilitate two 50 student 3 hour lab sessions a week

Oversee and facilitate the weekly lab training for lab TAs and lab assistants

Iteratively design and improve electrical engineering labs with professors and grad students

Design new labs for future semesters incorporating new lecture topics for the students

Conduct behavioral and technical interviews to hire new teacher assistants for future semesters.

Squishy Robotics - Electrical Engineer Intern:

May 2018 - August 2018

Redesign the robot's electrical stack to actuate a tensegrity, gather sensor data, and stream video

Prototype and design encoder boards, battery management systems, and audio/video multiplexing

Migrate the control stack away from Arduino to mbed and custom designed microcontroller boards

Work with mechanical engineers to devise creative mounting and enclosure solutions for electronics

Pure Storage - Hardware Intern:

June 2019 - August 2019

Collaborated with the Hardware Team to develop an automated diagnosis tool for RMA'd controllers

Developed a flexible infrastructure for log parsing and error catching to ease diagnosis

Extracurricular

Arias Research Group:

January 2018 - August 2020

Graduate Researcher under Professor Ana Arias - Physical and Printed Electronics

Designed a platform for printed electrodes and organic PPG module for wound monitoring

Design a force sensor calibration jig to help characterize different force sensitive printed sensors

Work to planarize PEN for plasma shadow masking contacts to print N-type OTFTs

Modularized the sensing platform for various types of printed sensors

Pioneers in Engineering - Hardware Advisor:

January 2016 - August 2020

Coordinate the supply chain for parts and components for the electrical teams

Have full stack understanding of the electrical system and advise teams

Onboarding new hardware staff to work on projects by training their Eagle and Git skills

Integrate adjacent software, mech, and electrical teams in a collaborative environment